SGM2324
1MHz, Quad, General Purpose CMOS Operational Amplifier

GENERAL DESCRIPTION

The SGM2324 is a quad, voltage feedback amplifier. The device can operate from 2.5V to 5.5V single supply. It provides rail-to-rail output voltage swing. This feature makes SGM2324 appropriate for buffering ASIC.

The SGM2324 offers a gain-bandwidth product of 1MHz and an ultra-low input bias current of 10pA. It is well suited for piezoelectric sensors, integrators and photodiode amplifiers.

The SGM2324 is designed into a wide range of applications, such as battery-powered instrumentation, safety monitoring, portable systems, and transducer interface circuits in low power systems.

The SGM2324 is available in Green SOIC-14 and TSSOP-14 packages. It is specified over the -40℃ to +85℃ temperature range.

FEATURES

- **Low Cost**
- **Input Offset Voltage: 1.7mV (TYP)**
- **Ultra-Low Input Bias Current: 10pA**
- **Unity-Gain Stable**
- **Gain-Bandwidth Product: 1MHz**
- **Rail-to-Rail Output**
- **Supply Voltage Range: 2.5V to 5.5V**
- **Minimum Input Common Mode Voltage below 0V**
- **-40℃ to +85℃ Operating Temperature Range**
- **Available in Green SOIC-14 and TSSOP-14 Packages**

APPLICATIONS

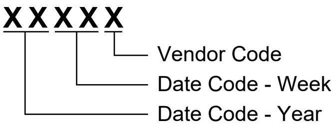
ASIC Input or Output Amplifiers
Piezoelectric Transducer Amplifiers
Battery-Powered Equipment
Portable Equipment
Sensor Interfaces
Medical Instrumentation
Mobile Communications
Smoke Detectors
Notebook PCs
PCMCIA Cards
DSP Interfaces

PACKAGE/ORDERING INFORMATION

MODEL	PACKAGE DESCRIPTION	SPECIFIED TEMPERATURE RANGE	ORDERING NUMBER	PACKAGE MARKING	PACKING OPTION
SGM2324	SOIC-14	-40℃ to +85℃	SGM2324YS14/TR	SGM2324YS14 XXXXX	Tape and Reel, 2500
	TSSOP-14	-40℃ to +85℃	SGM2324YTS14/TR	SGM2324 YTS14 XXXXX	Tape and Reel, 3000

MARKING INFORMATION

NOTE: XXXXX = Date Code and Vendor Code.



Green (RoHS & HSF): SG Micro Corp defines "Green" to mean Pb-Free (RoHS compatible) and free of halogen substances. If you have additional comments or questions, please contact your SGMICRO representative directly.

ABSOLUTE MAXIMUM RATINGS

Supply Voltage, $+V_S$ to $-V_S$ 6.0V
Junction Temperature .... +160℃
Storage Temperature Range .... -65℃ to +150℃
Lead Temperature (Soldering, 10s) .... +260℃
ESD Susceptibility
HBM 4000V
MM 400V

RECOMMENDED OPERATING CONDITIONS

Operating Temperature Range .... -40℃ to +85℃

OVERSTRESS CAUTION

Stresses beyond those listed in Absolute Maximum Ratings may cause permanent damage to the device. Exposure to absolute maximum rating conditions for extended periods may affect reliability. Functional operation of the device at any conditions beyond those indicated in the Recommended Operating Conditions section is not implied.

ESD SENSITIVITY CAUTION

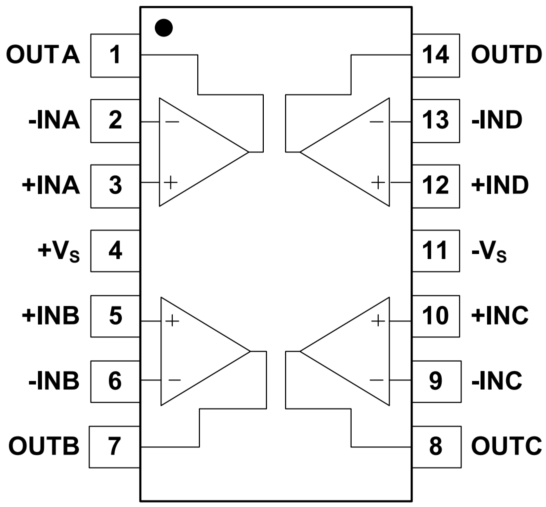
This integrated circuit can be damaged if ESD protections are not considered carefully. SGMICRO recommends that all integrated circuits be handled with appropriate precautions. Failure to observe proper handling and installation procedures can cause damage. ESD damage can range from subtle performance degradation to complete device failure. Precision integrated circuits may be more susceptible to damage because even small parametric changes could cause the device not to meet the published specifications.

DISCLAIMER

SG Micro Corp reserves the right to make any change in circuit design, or specifications without prior notice.

PIN CONFIGURATIONS

(TOP VIEW)



TSSOP-14/SOIC-14

ELECTRICAL CHARACTERISTICS

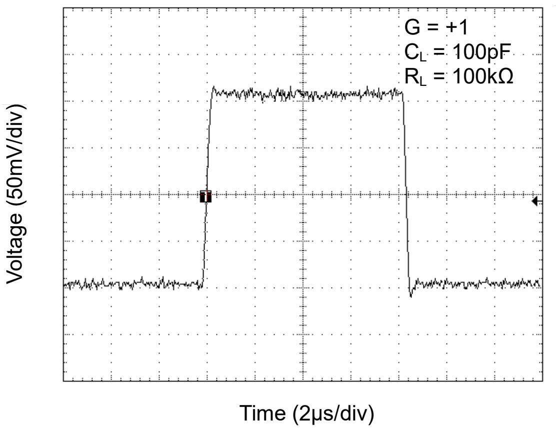
(V_S = 5V, at R_L = 100kΩ connected to $V_S/2$ and V_{OUT} = $V_S/2$, unless otherwise noted.)

PARAMETER	CONDITIONS	SGM2324 TYP +25℃	MIN/MAX OVER TEMPERATURE +25℃	-40℃ to +85℃	UNITS	MIN/MAX
Input Characteristics						
Input Offset Voltage (V_{OS})		1.7	10	12	mV	MAX
Input Bias Current (I_B)		10			pA	TYP
Input Offset Current (I_{OS})		10			pA	TYP
Input Common Mode Voltage Range	V_S = 5.5V		-0.1		V	MIN
			4		V	MAX
Common Mode Rejection Ratio (CMRR)	V_S = 5V, V_{CM} = -0.1V to 3.3V	88	65	50	dB	MIN
Open-Loop Voltage Gain (A_{OL})	R_L = 2kΩ, V_{OUT} = 0.1V to 4.9V	100	85	80	dB	MIN
	R_L = 10kΩ, V_{OUT} = 0.035V to 4.965V	110	90	85	dB	MIN
Input Offset Voltage Drift ($\Delta V_{OS}/\Delta T$)		3.5			μV/℃	TYP
Output Characteristics						
Output Voltage Swing from Rail	V_{OUT} Connect R_L (2kΩ) to GND	0.080			V	TYP
	V_{OUT} Connect R_L (2kΩ) to $+V_S$	0.087			V	TYP
	V_{OUT} Connect R_L (10kΩ) to GND	0.008			V	TYP
	V_{OUT} Connect R_L (10kΩ) to $+V_S$	0.015			V	TYP
Output Current (I_{OUT})	V_{OUT} = $+V_S$ - 0.5V	13			mA	TYP
	V_{OUT} = $-V_S$ + 0.5V	-8			mA	TYP
Short Circuit Current (I_{SC})	V_{OUT} Connect R_L (10Ω) to GND	43	28	24	mA	MIN
	V_{OUT} Connect R_L (10Ω) to $+V_S$	-33	-18	-16	mA	MAX
Power Supply						
Operating Voltage Range			2.5	2.5	V	MIN
			5.5	5.5	V	MAX
Power Supply Rejection Ratio (PSRR)	V_S = 2.5V to 5.5V					
	V_{CM} = ($-V_S$) + 0.5V	80	75	70	dB	MIN
Quiescent Current (I_Q)	I_{OUT} = 0	0.65	1.2	1.3	mA	MAX
Dynamic Performance						
Gain-Bandwidth Product (GBP)		1			MHz	TYP
Slew Rate (SR)	G = +1, 2V Output step	0.65			V/μs	TYP
Settling Time to 0.1% (t_S)	G = +1, 2V Output step	9.0			μs	TYP
Overload Recovery Time	V_{IN} · Gain = V_S	4.0			μs	TYP
Crosstalk	f = 1kHz	-80			dB	TYP
	f = 1MHz	-65			dB	TYP
Noise Performance						
Input Voltage Noise Density (e_n)	f = 1kHz	42.0			nV/$\sqrt{Hz}$	TYP
	f = 10kHz	38.0			nV/$\sqrt{Hz}$	TYP

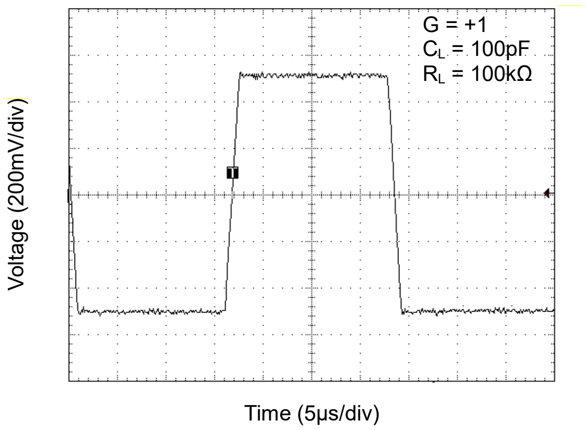
TYPICAL PERFORMANCE CHARACTERISTICS

At T_A = +25℃, V_S = 5V, and R_L = 100kΩ connected to V_S/2, unless otherwise noted.

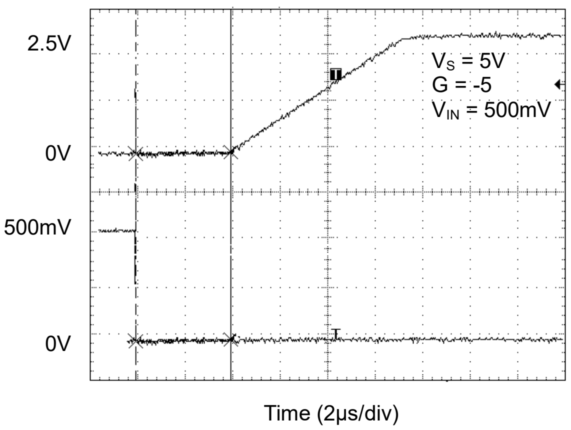
Small-Signal Step Response



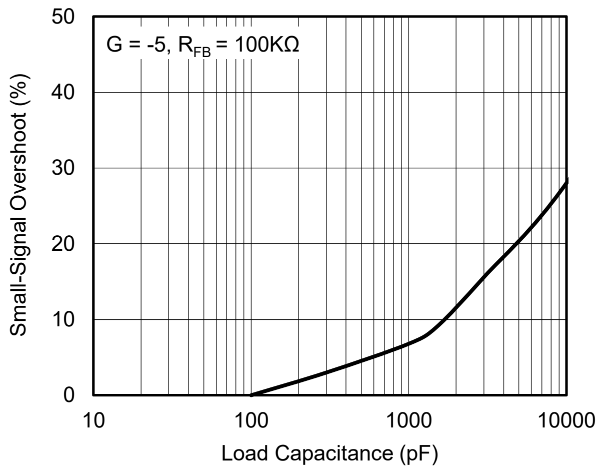
Large-Signal Step Response



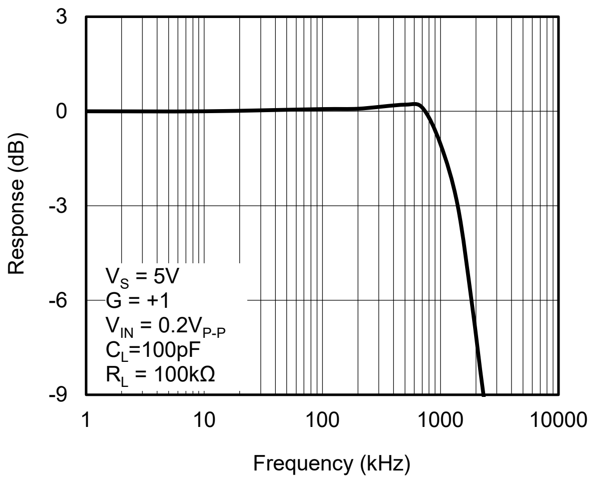
Overload Recovery Time



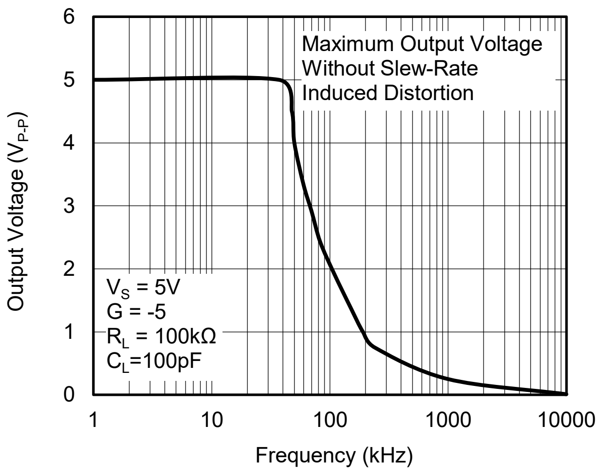
Small-Signal Overshoot vs. Load Capacitance



Response vs. Frequency



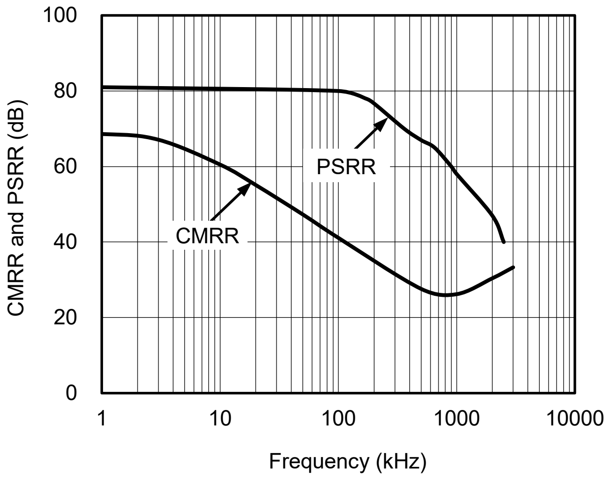
Maximum Output Voltage vs. Frequency



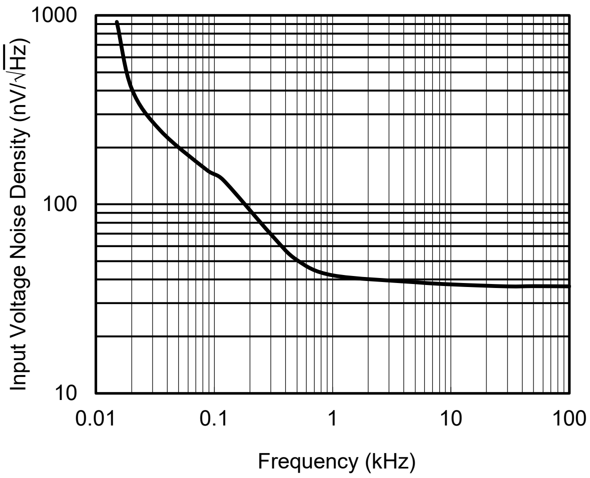
TYPICAL PERFORMANCE CHARACTERISTICS (continued)

At T_A = +25℃, V_S = 5V, and R_L = 100kΩ connected to V_S/2, unless otherwise noted.

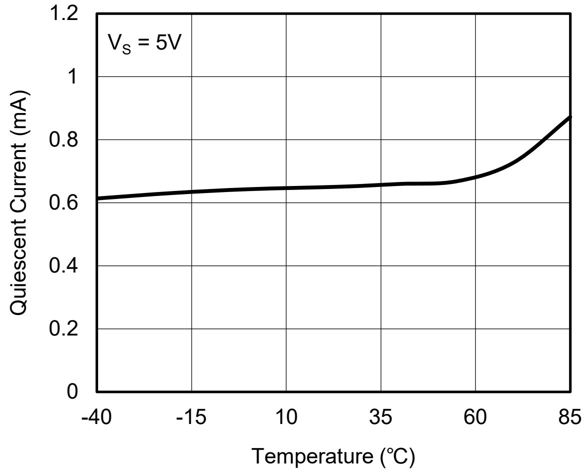
CMRR and PSRR vs. Frequency



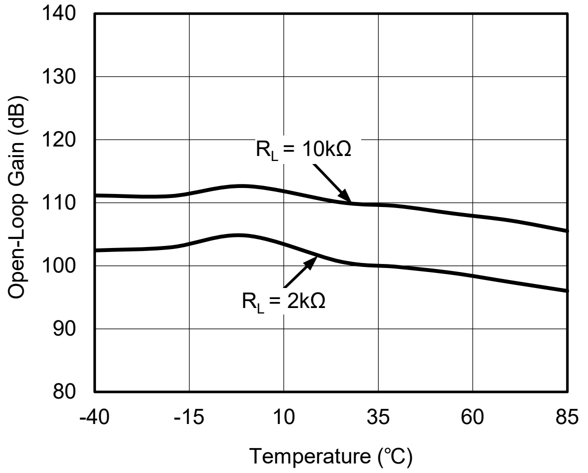
Input Voltage Noise Density vs. Frequency



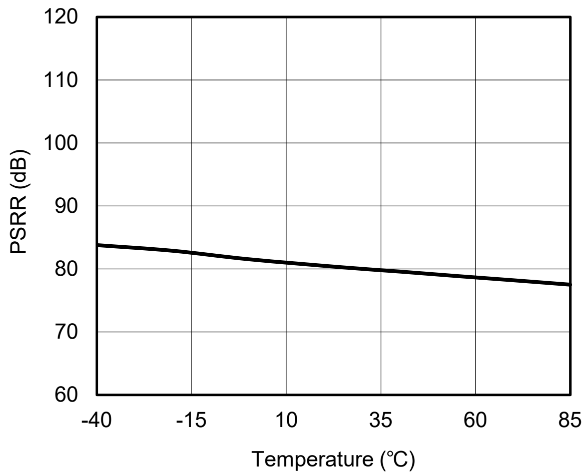
Quiescent Current vs. Temperature



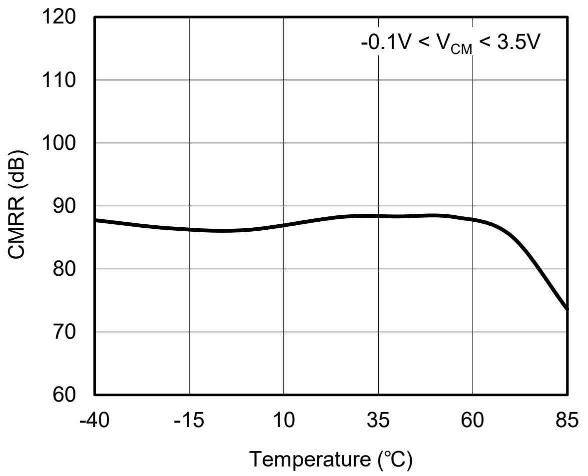
Open-Loop Gain vs. Temperature



Power Supply Rejection Ratio vs. Temperature

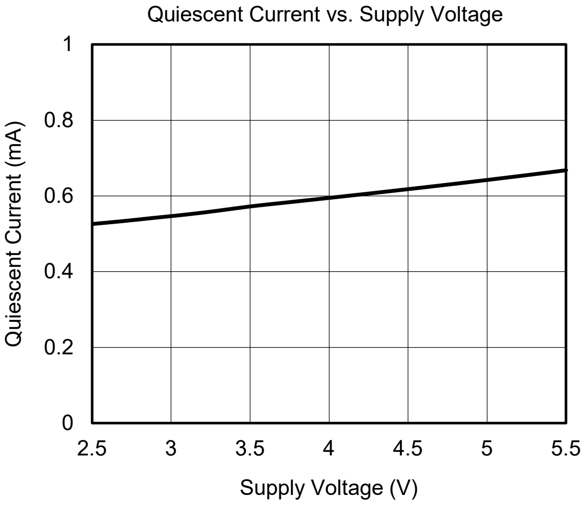


Common Mode Rejection Ratio vs. Temperature



TYPICAL PERFORMANCE CHARACTERISTICS (continued)

At T_A = +25℃, V_S = 5V, and R_L = 100kΩ connected to $V_S/2$, unless otherwise noted.



APPLICATION INFORMATION

Rail-to-Rail Output

The SGM2324 supports rail-to-rail output operation. In single power supply application, for example, when $+V_S$ = 5V, $-V_S$ = GND, 10kΩ load resistor is tied from OUT pin to ground, the typical output swing range is from 0.008V to 4.992V.

Driving Capacitive Loads

The SGM2324 is designed for unity-gain stable for capacitive load up to 250pF. If greater capacitive load must be driven in application, the circuit in Figure 1 can be used. In this circuit, the IR drop voltage generated by R_{ISO} is compensated by feedback loop.

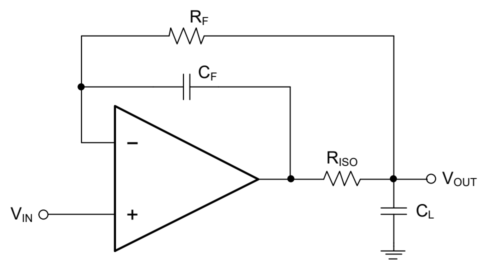


Figure 1. Circuit to Drive Heavy Capacitive Load

Power Supply Decoupling and Layout

A clean and low noise power supply is very important in amplifier circuit design, besides of input signal noise, the power supply is one of important source of noise to the amplifier through $+V_S$ and $-V_S$ pins. Power supply bypassing is an effective method to clear up the noise at power supply, and the low impedance path to ground of decoupling capacitor will bypass the noise to GND. In application, 10μF ceramic capacitor paralleled with 0.1μF or 0.01μF ceramic capacitor is used in Figure 2. The ceramic capacitors should be placed as close as possible to $+V_S$ and $-V_S$ power supply pins.

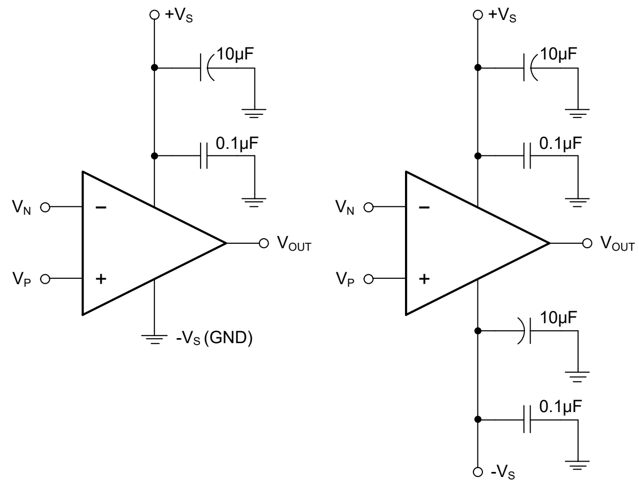


Figure 2. Amplifier Power Supply Bypassing

APPLICATION INFORMATION (continued)

Typical Application Circuits

Difference Amplifier

The circuit in Figure 3 is a design example of classical difference amplifier. If $R_4/R_3 = R_2/R_1$, then $V_{OUT} = (V_P - V_N) \times R_2/R_1 + V_{REF}$.

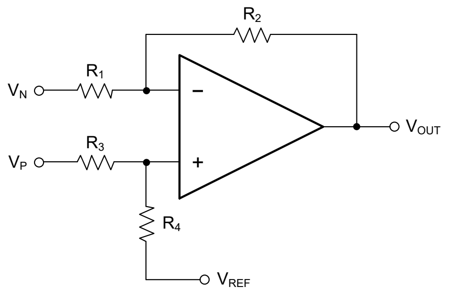


Figure 3. Difference Amplifier

High Input Impedance Difference Amplifier

The circuit in Figure 4 is a design example of high input impedance difference amplifier, the added amplifiers at the input are used to increase the input impedance and eliminate drawback of low input impedance in Figure 3.

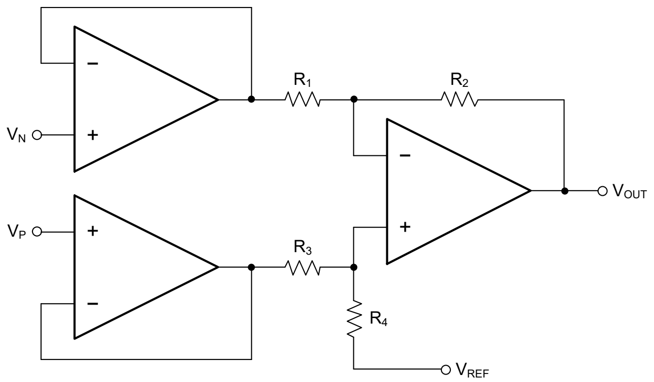


Figure 4. High Input Impedance Difference Amplifier

Active Low-Pass Filter

The circuit in Figure 5 is a design example of active low-pass filter, the DC gain is equal to $-R_2/R_1$ and the -3dB corner frequency is equal to $1/2\pi R_2C$. In this design, the filter bandwidth must be less than the bandwidth of the amplifier, the resistor values must be selected as low as possible to reduce ringing or oscillation generated by the parasitic parameters in PCB layout.

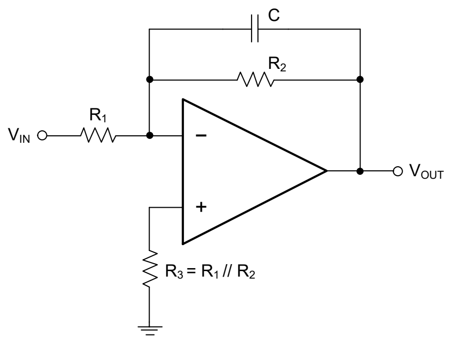


Figure 5. Active Low-Pass Filter

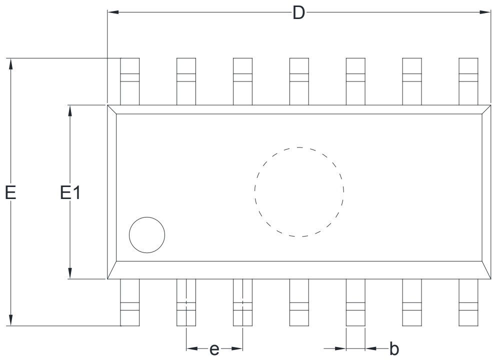
REVISION HISTORY

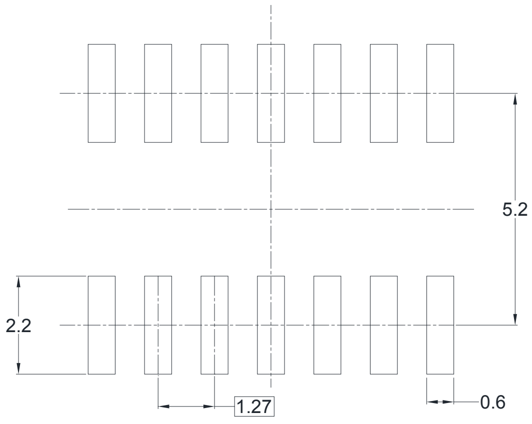
NOTE: Page numbers for previous revisions may differ from page numbers in the current version.

JANUARY 2013 – REV.B.3 to REV.B.4	Page
Added Package Outline Dimensions section	10, 11
Added Tape and Reel Information section	12, 13

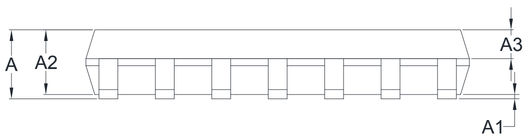
PACKAGE OUTLINE DIMENSIONS

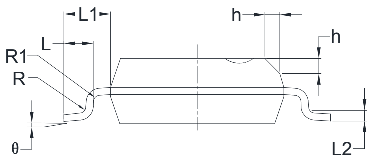
SOIC-14





RECOMMENDED LAND PATTERN (Unit: mm)

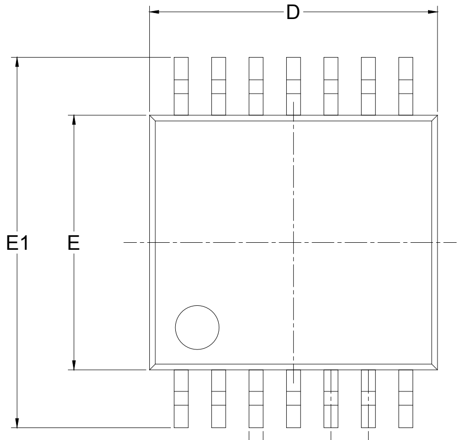


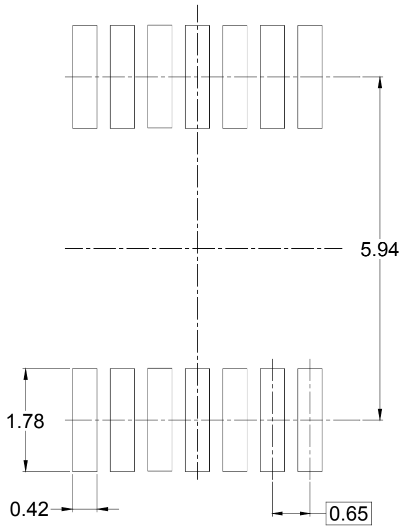


Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	**MAX**	**MIN**	**MAX**
A	1.35	1.75	0.053	0.069
A1	0.10	0.25	0.004	0.010
A2	1.25	1.65	0.049	0.065
A3	0.55	0.75	0.022	0.030
b	0.36	0.49	0.014	0.019
D	8.53	8.73	0.336	0.344
E	5.80	6.20	0.228	0.244
E1	3.80	4.00	0.150	0.157
e	1.27 BSC		0.050 BSC	
L	0.45	0.80	0.018	0.032
L1	1.04 REF		0.040 REF	
L2	0.25 BSC		0.01 BSC	
R	0.07		0.003	
R1	0.07		0.003	
h	0.30	0.50	0.012	0.020
θ	0°	8°	0°	8°

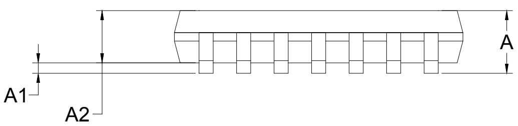
PACKAGE OUTLINE DIMENSIONS

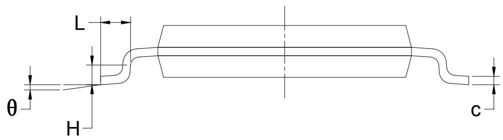
TSSOP-14





RECOMMENDED LAND PATTERN (Unit: mm)

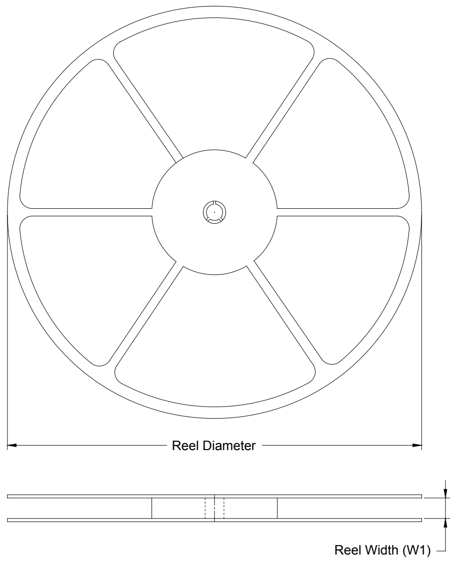




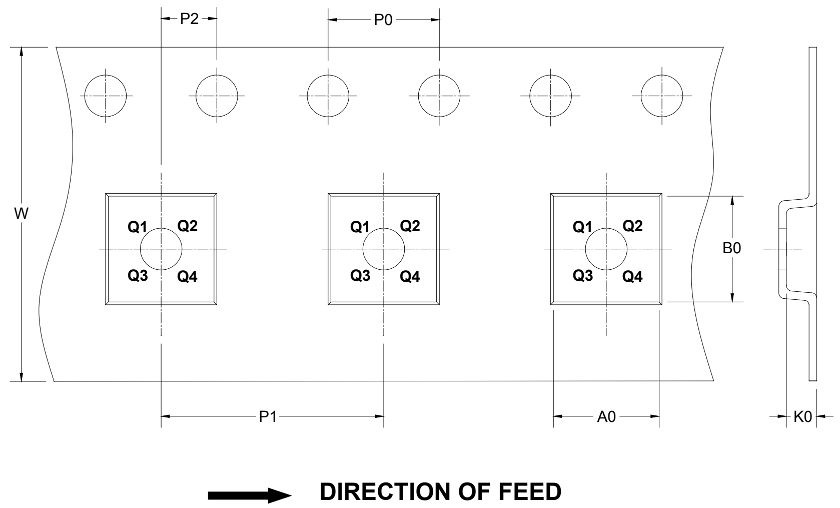
Symbol	Dimensions In Millimeters		Dimensions In Inches	
	MIN	**MAX**	**MIN**	**MAX**
A		1.200		0.047
A1	0.050	0.150	0.002	0.006
A2	0.800	1.050	0.031	0.041
b	0.190	0.300	0.007	0.012
c	0.090	0.200	0.004	0.008
D	4.860	5.100	0.191	0.201
E	4.300	4.500	0.169	0.177
E1	6.250	6.550	0.246	0.258
e	0.650 BSC		0.026 BSC	
L	0.500	0.700	0.02	0.028
H	0.25 TYP		0.01 TYP	
θ	1°	7°	1°	7°

TAPE AND REEL INFORMATION

REEL DIMENSIONS



TAPE DIMENSIONS



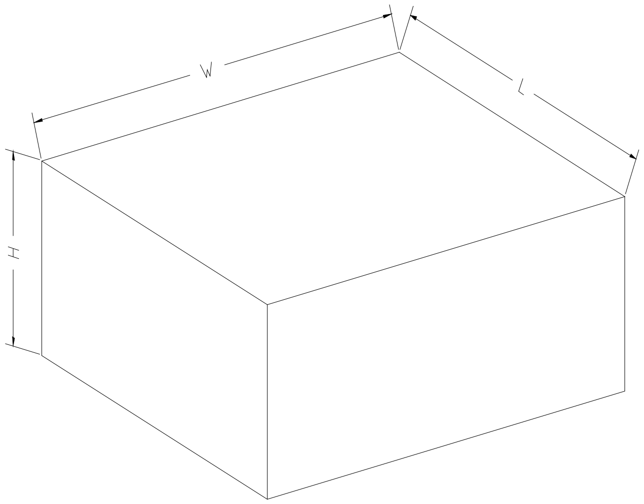
NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF TAPE AND REEL

Package Type	Reel Diameter	Reel Width W1 (mm)	A0 (mm)	B0 (mm)	K0 (mm)	P0 (mm)	P1 (mm)	P2 (mm)	W (mm)	Pin1 Quadrant
SOIC-14	13″	16.4	6.60	9.30	2.10	4.0	8.0	2.0	16.0	Q1
TSSOP-14	13″	12.4	6.95	5.60	1.20	4.0	8.0	2.0	12.0	Q1

DD0001

CARTON BOX DIMENSIONS



NOTE: The picture is only for reference. Please make the object as the standard.

KEY PARAMETER LIST OF CARTON BOX

Reel Type	Length (mm)	Width (mm)	Height (mm)	Pizza/Carton
7″ (Option)	368	227	224	8
7″	442	410	224	18
13″	386	280	370	5

DD0002